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Lecture 1: Introduction

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A. Manian and B. Razavi, "A 32-Gb/s 9.3-mW CMOS Equalizer with 0.73-V Supply," Proc. IEEE Custom Integrated Circuits Conference, Sept. 2014. B. Razavi, "The Role of Translational Circuits in RF Receiver Design," Proc. IEEE Custom Integrated Circuits Conference, Sept. 2014. S. Hwu and B. Razavi, "A Receiver Architecture for Intra-Band Carrier Aggregation," Symposium on VLSI Circuits ...

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(a) Calculate the drain current ID of the transistor in the amplifier (neglect channellength modulation). Verify your result by SPICE (use the .op analysis). (b) Calculate the small-signal gain vout/vin by hand calculation. Using the .tran analysis in SPICE to verify your result, assuming CCI, CCZ, and CS are infinite (using C= 1 F in simulation).

Solved: Microelectronics SPICE Problem: (use Large-signal ...

Behzad Razavi received the B.Sc. degree in electrical engineering from Sharif University of Technology in 1985, and the M.Sc. and Ph.D. degrees in electrical engineering from Stanford University in 1988 and 1992, respectively. He was with AT&T Bell Laboratories and subsequently Hewlett-Packard Laboratories until 1996. He was also an Adjunct Professor at Princeton University from 1992 to 1994.

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