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Spartan-6 FPGA SP605 Evaluation Kit - Xilinx

The SP605 evaluation kit provides integration of hardware, software, intellectual property (IP), and pre-verified reference designs so development can begin right out of the box. The Kit includes a flexible environment for system design with applications that implement features such as high-speed serial transceivers, a PCI Express ® interface ...

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AXI Interface Based ML605/SP605 MicroBlaze Processor Subsystem

The SP605 board enables hardware and software developers to create or evaluate designs targeting the Spartan®-6 XC6SLX45T-3FGG484 FPGA. The SP605 provides board features common to many embedded processing systems.

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Spartan-6 device block RAM capabilities. SP605 Hardware User Guide - Xilinx The SP601 provides board features for evaluating the Spartan-6 family that are common to Xilinx UG518 SP601 Hardware User Guide, User Guide WWW.XILINX.COM/SP601 This Hardware Setup Guide will provide the step by step

Sp601 Hardware User Guide - [e13 Components](http://e13.com)

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Preface: About This Guide 8 www.xilinx.com SP605 Hardware User Guide UG526 (v1.9) February 14, 2019 This guide describes the clocking resources available in all Spartan-6 devices, including the DCMs and PLLs.

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